Max. Marks 100

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USN

Seventh Semester B.E. Degree Examination, Dec.2014/Jan.2015

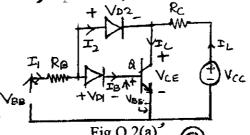
Power Electronics

Note: Answer any FIVE full questions, selecting

atleast TWO questions from each part.

PART - A

- Mentionary four properties of a super power device should possess. (02 Marks)
- What is Sepverter? How are power converters classified? Explain briefly. (08 Marks) b. Mention some important advantages and disadvantages of power converters. (04 Marks)
- C. What are the poppheral effects of power electronics converters and how they are overcome? d. (06 Marks)
- The collector clamping circuit of Fig.Q.2(a) has following parameters: $V_{BB} = 14V$, $R_{B} = 3\Omega$, $\beta = 15$, $V_{BE} = 7V$, $V_{D2} = 0.9V$, $V_{D1} = 2.1V$, $R_{C} = 2\Omega$, $V_{CC} = 120V$, Find: i) Collector current without damping; ii) Collector-emitter clamping voltage; iii) Collector (05 Marks) current with clamping.



Draw the switching model of MOSFET and explain its switching characteristics with neat figure. (05 Marks)

- Compare IGBT with BJT and MOSFET. What is an IGRT c.
- Why isolation is needed? Explain the two methods of isolation. (04 Marks)
- that figure explain the dynamic turn-on and turn-off characteristics of a thyristor. 3 a.
 - (08 Marks) (06 Marks) Fiefly explain di/dt and dv/dt protection of SCR.
 - Design a UJT relaxation oscillator for triggering an SCR, with UJT having the following parameters $\eta = 0.72$, $I_P = 60\mu A$, $I_V = 4mA$, $V_v = 2.5V$, $V_{BB} = 15V$, $R_{BB} = 5K\Omega$, leakage current with emitter open is 3mA. Also calculate minimum and maximum value RC (06 Marks) variable resistance).
 - With a circuit diagram and waveforms explain the working of a single phase semi controlled rectifier. Derive an expression for the average voltage across the R-L load.
 - b. For a single phase fully controlled bridge rectifier with highly inductive load and continuous current, obtain average load voltage and current if the load resistance is 10Ω and firing angle is 45°, and is fed from 230V, 50Hz supply. Draw the load voltage waveform and supply (06 Marks) current waveform.
 - What is a dual converter? Explain its operation with a neat circuit diagram. (06 Marks)

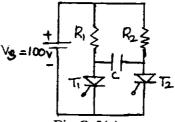
State the conditions to be satisfied for proper turn-off of SCR.

(02 Marks)

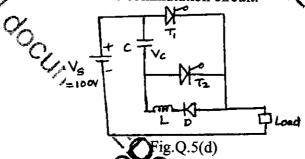
With the help of circuit diagram and waveforms explain the operation of self commutation.

(06 Marks)

In the Fig.Q.5(c) the source voltage $V_s = 100V$ and the current through R_1 and R_2 is 25A. The turn off time of both the SCRs is 40µsec. Find the value of capacitor for successful commutation and hence show that circuit turn off time is 0.693 RC.



A08.48i. rig. Q.5(d) the bare and the time is 40 and the tim voltage is 100V. Maximum load current is 40A and thyristor turn off time is 40 psec. Assume 50% tolerance on turn off the Find L and C of the commutation circuit. (04 Marks)



Distinguish between on-off control of AC voltage controller. 6 (04 Marks)

Explain the operation of single phase bidirectional AC voltage controller for inductive load with the help of circuit diagram and waveforms.

- An AC voltage control has a resistive load $\delta R = 10\Omega$, and RMS input voltage is $V_S = 120V$, 50Hz. The thyristor switch is on for n = 60 cycles and off form = 75 cycles. Find: i) rms output voltage; ii) Input power factor; iii) Average and RMS thyristor current and hence derive the above expressions for Vorm and PF. (10 Marks)
- Explain the working principle of step-down chopper and derive expression for i) Average 7 output stage; ii) Output power; iii) Effective input resistance in terms of chopper duty (08 Marks)

the help of a circuit diagram, explain four quadrant type E choppers. (08 Marks) step up chopper has input voltage of 220V and output voltage of 660V If the non conducting time of the thyristor is 100µsec, compute the pulse width of the output poltage. If the pulse width is halved for constant frequency operation, find new output voltage:

- Explain a single phase full bridge inverter with relevant circuit diagram and waveforms Assume R-L load. (08 Marks)
- b. With the help of circuit diagram and wave forms explain the operation of transistorized CSI (current source inverter). What are the advantages and disadvantages of CSI? (08 Marks)
- The single phase half bridge inverter has the DC input of 48V. The load resistance is 4.8Ω . Determine: i) RMS value of the output voltage; ii) RMS value of the fundamental component; iii) Total harmonic distortion. (04 Marks)